


MCU_ABC

UIB

LCD_G3	FMC_D7	PA0	M1	PA0/TIM2_CH1/TIM5_CH1/TIM9_CH1/TIM15_BKIN/SPI6_NSS/I2S6_WS/USART2_CTS/USART2_NSS/UART4_TX/SDMMC2_CMD/SAI2_SD_B/FMC_AD7/FMC_D7/LCD_G3/ADC12_INP0/ADC12_INN1/WKUP1
LCD_G2	FMC_D6	PA1	M2	PA1/TIM2_CH2/TIM5_CH2/LPTIM3_IN1/TIM15_CH1N/DCMIPP_D0/PSSI_D0/USART2_RTS/UART4_RX/SAI2_MCLK_B/ETH_MII_RX_CLK/ETH_RMII_REF_CLK/FMC_AD6/FMC_D6/LCD_G2/ADC12_INP1
LCD_B7	FMC_D5	PA2	L4	PA2/TIM2_CH3/TIM5_CH3/LPTIM3_IN2/TIM15_CH1/USART2_TX/SAI2_SCK_B/ETH_MDIO/FMC_AD5/FMC_D5/LCD_B7/MDIOS_MDIO/ADC12_INP14/WKUP2
	PWM_DAC	PA3	N1	PA3/TIM2_CH4/TIM5_CH4/LPTIM3_CH1/TIM15_CH2/I2S6_MCK/SPI4_RDY/USART2_RX/GFXTIM_LCKCAL/SPI5_RDY/SPI1_RDY/ETH_MII_COL/GFXTIM_FCLKCAL/LCD_DE/TIM1_CH3/ADC12_INP15
	ETH_MDIO	PA4	M3	PA4/TIM5_ETR/LPTIM3_CH2/SPI1_NSS/I2S1_WS/SPI3_NSS/I2S3_WS/USART2_CK/SPI6_NSS/I2S6_WS/PSSI_DE/OTG_HS_SOF/ETH_MDIO/LCD_R3/DCMIPP_HSYNC/ADC1_INP18
LCD_CLK	FMC_NOE	PA5	N2	PA5/PWR_CSTOP/TIM2_CH1/TIM2_ETR/TIM9_CH2/SPI1_SCK/I2S1_CK/PSSI_D8/SPI6_SCK/I2S6_CK/FMC_NOE/DCMIPP_D8/LCD_CLK/ADC2_INP18
	DCMIPP_PCLK	PA6	P1	PA6/PWR_CSLEEP/TIM1_BKIN/TIM3_CH1/LPTIM3_ETR/SPI1_MISO/I2S1_SDI/PSSI_PDCK/SPI6_MISO/I2S6_SDI/TIM13_CH1/MDIOS_MDC/LCD_B7/DCMIPP_PIXCLK/LCD_HSYNC/ADC12_INP3
	RMII_CRS_DV	PA7	M4	PA7/TIM1_CH1N/TIM3_CH2/SPI1_MOSI/I2S1_SDO/SPI6_MOSI/I2S6_SDO/TIM14_CH1/LCD_R4/ETH_MII_RX_DV/ETH_RMII_CRS_DV/FMC_INT/LCD_B1/ADC12_INP7/ADC12_INN3
LCD_B6	FMC_D4	PA8	F12	PA8/MCO1/TIM1_CH1/I2C3_SCL/USART1_CK/OTG_FS_SOF/UART7_RX/FMC_AD4/FMC_D4/LCD_B6
LCD_B5	FMC_D3	PA9	D14	PA9/TIM1_CH2/LPUART1_TX/I2C3_SDA/SPI2_SCK/I2S2_CK/PSSI_D0/USART1_TX/FMC_AD3/FMC_D3/DCMIPP_D0/LCD_B5
LCD_B4	FMC_D2	PA10	E13	PA10/TIM1_CH3/LPUART1_RX/PSSI_D1/USART1_RX/MDIOS_MDIO/FMC_AD2/FMC_D2/DCMIPP_D1/LCD_B4
LCD_B3	FMC_D1	PA11	C15	PA11/TIM1_CH4/LPUART1_CTS/SPI2_NSS/I2S2_WS/UART4_RX/USART1_CTS/USART1_NSS/FDCAN1_RX/FMC_AD1/FMC_D1/LCD_B3
LCD_B2	FMC_D0	PA12	C14	PA12/TIM1_ETR/LPUART1_RTS/SPI2_SCK/I2S2_CK/UART4_TX/USART1_RTS/SAI2_FS_B/FDCAN1_TX/FMC_AD0/FMC_D0/LCD_B2
	SWDIO	PA13	E12	PA13/JTMS/SWDIO
	SWDCLK	PA14	D13	PA14/JTCK/SWCLK
LCD_R5	FMC_D15	PA15	G11	PA15/JTDI/TIM2_CH1/TIM2_ETR/HDMI_CEC/SPI1_NSS/I2S1_WS/SPI3_NSS/I2S3_WS/SPI6_NSS/I2S6_WS/UART4_RTS/UART7_TX/FMC_D15/FMC_AD15/LCD_R5
REMOTE_IN	DCMIPP_XCLK	PB0	P2	PB0/TIM1_CH2N/TIM3_CH3/TIM9_CH1/SPI1_SCK/I2S1_CK/UART4_CTS/ETH_MII_TXD0/ETH_RMII_TXD0/GFXTIM_TE/LCD_VSYNC/ADC12_INP9/ADC12_INN5
	RGB_LED	PB1	R2	PB1/TIM1_CH3N/TIM3_CH4/TIM9_CH2/FDCAN2_TX/LCD_G2/ETH_MII_TXD1/ETH_RMII_TXD1/FMC_NOE/ADC12_INP5
	FMC_NWE	PB2	N4	PB2/RTC_OUT2/SAI1_D1/ADFI_DATIN0/SAI1_SD_A/SPI3_MOSI/I2S3_SDO/LCD_B2/FMC_NWE
LCD_R4	FMC_D14	PB3	C4	PB3/JTDO/SWO/TIM2_CH2/LPTIM4_IN1/DCMIPP_HSYNC/SPI1_SCK/I2S1_CK/SPI3_SCK/I2S3_CK/SPI6_SCK/I2S6_CK/SDMMC2_D2/CRS_SYNC/UART7_RX/FMC_D14/FMC_AD14/LCD_R4/PSSI_DE
LCD_R3	FMC_D13	PB4	D5	PB4/NTRST/TIM16_BKIN/TIM3_CH1/LPTIM4_ETR/DCMIPP_VSYNC/SPI1_MISO/I2S1_SDI/SPI3_MISO/I2S3_SDI/SPI2_NSS/I2S2_WS/SPI6_MISO/I2S6_SDI/SDMMC2_D3/UART7_TX/FMC_D13/FMC_AD13/LCD_R3/PSSI_RDY
LCD_R2	FMC_D12	PB5	B3	PB5/TIM17_BKIN/TIM3_CH2/LPTIM4_OUT/I2C1_SMBA/SPI1_MOSI/I2S1_SDO/PSSI_D10/SPI3_MOSI/I2S3_SDO/SPI6_MOSI/I2S6_SDO/FDCAN2_RX/LCD_R2/ETH_PPS_OUT/FMC_D12/FMC_AD12/DCMIPP_D10/UART5_RX
	DCMIPP_D5	PB6	E5	PB6/TIM16_CH1N/TIM4_CH1/I2C1_SCL/I3C1_SCL/HDMI_CEC/PSSI_D5/USART1_TX/LPUART1_TX/FDCAN2_TX/ETH_MII_RX_CLK/ETH_RMII_REF_CLK/FMC_SDNE1/DCMIPP_D5/UART5_TX
	DCMIPP_VSYNC	PB7	D4	PB7/TIM17_CH1N/TIM4_CH2/I2C1_SDA/I3C1_SDA/DCMIPP_D1/PSSI_RDY/USART1_RX/LPUART1_RX/PSSI_D1/ETH_MII_TXD1/ETH_RMII_TXD1/FMC_SDCKE1/DCMIPP_VSYNC/UART5_TX
	DCMIPP_D6	PB8	B1	PB8/TIM16_CH1/TIM4_CH3/USART3_CK/I2C1_SCL/I3C1_SCL/PSSI_D6/SDMMC1_CKIN/UART4_RX/FDCAN1_RX/SDMMC2_D4/ETH_MII_TXD3/SDMMC1_D4/DCMIPP_D6/FMC_D9/FMC_AD9
	DCMIPP_D7	PB9	C2	PB9/TIM17_CH1/TIM4_CH4/I2C1_SDA/I3C1_SDA/SPI2_NSS/I2S2_WS/PSSI_D7/SDMMC1_CDIR/UART4_TX/FDCAN1_TX/SDMMC2_D5/SDMMC1_D5/DCMIPP_D7/TAMP_IN2/TAMP_OUT1
LCD_G7	FMC_D11	PB10	P15	PB10/TIM2_CH3/LPTIM2_IN1/I2C2_SCL/SPI2_SCK/I2S2_CK/USART3_TX/ETH_MII_RX_ER/FMC_D11/FMC_AD11/LCD_G7
LCD_G6	FMC_D10	PB11	N14	PB11/TIM2_CH4/LPTIM2_ETR/I2C2_SDA/USART3_RX/ETH_MII_TX_EN/ETH_RMII_TX_EN/FMC_D10/FMC_AD10/LCD_G6
LCD_G5	FMC_D9	PB12	L12	PB12/TIM1_BKIN/LPTIM2_IN2/I2C2_SMBA/SPI2_NSS/I2S2_WS/USART3_CK/FDCAN2_RX/FMC_D9/FMC_AD9/LCD_G5/UART5_RX
LCD_G4	FMC_D8	PB13	N15	PB13/TIM1_CH1N/LPTIM2_CH1/SPI2_SCK/I2S2_CK/SDMMC1_D0/USART3_CTS/USART3_NSS/PSSI_D2/FDCAN2_TX/LCD_G4/ETH_MII_RXD3/FMC_D8/FMC_AD8/DCMIPP_D2/UART5_TX
	USART1_TX	PB14	M13	PB14/TIM1_CH2N/TIM12_CH1/LPTIM2_CH2/USART1_TX/SPI2_MISO/I2S2_SDI/USART2_RTS/UART4_RTS/SDMMC2_D0/FMC_NE1/LCD_DE
	USART1_RX	PB15	M14	PB15/RTC_REFIN/TIM1_CH3N/TIM12_CH2/USART1_RX/SPI2_MOSI/I2S2_SDO/UART4_CTS/SDMMC2_D1/LCD_G7/FMC_A20
	STM_ADC	PC0	K3	PC0/GFXTIM_FCLKCAL/SAI2_FS_B/FMC_NBL1/GFXTIM_LCKCAL/ADC12_INP10
	ETH_MDC	PC1	L1	PC1/TRACED0/SAI1_D1/ADFI_DATIN0/SPI2_MOSI/I2S2_SDO/SAI1_SD_A/FMC_A16/SDMMC2_CK/ETH_MDC/FMC_A0/MDIOS_MDC/ADC12_INP11/ADC12_INN10/TAMP_IN7/TAMP_OUT8/WKUP4
	SPI2_MISO	PC2	L2	PC2/TIM1_CH1/SPI2_MISO/I2S2_SDI/FMC_A17/ETH_MII_TXD2/FMC_A1/ADC12_INP12/ADC12_INN11
	SPI2_MOSI	PC3	L3	PC3/TIM1_CH2/SPI2_MOSI/I2S2_SDO/FMC_A18/ETH_MII_TX_CLK/FMC_A2/ADC12_INP13/ADC12_INN12
	RMII_RXD0	PC4	L5	PC4/I2S1_MCK/FMC_A19/SPDIFRX_IN3/ETH_MII_RXD0/ETH_RMII_RXD0/FMC_A3/ADC12_INP4
	RMII_RXD1	PC5	N3	PC5/SAI1_D3/DCMIPP_D15/PSSI_D15/FMC_A21/SPDIFRX_IN4/ETH_MII_RXD1/ETH_RMII_RXD1/FMC_A5/ADC12_INP8/ADC12_INN4
	DCMIPP_D0	PC6	E15	PC6/TIM3_CH1/TIM9_CH1/I2S2_MCK/SDMMC1_D0DIR/PSSI_D0/SDMMC2_D6/SDMMC1_D6/DCMIPP_D0
	DCMIPP_D1	PC7	F13	PC7/TRGIO/TIM3_CH2/TIM9_CH2/I2S3_MCK/SDMMC1_D123DIR/PSSI_D1/SDMMC2_D7/SDMMC1_D7/DCMIPP_D1
SDIO_D0		PC8	D15	PC8/TRACED1/TIM3_CH3/I2C3_SMBA/UART5_RTS/PSSI_D2/SDMMC1_D0/DCMIPP_D2
SDIO_D1		PC9	E14	PC9/MCO2/TIM3_CH4/I2C3_SDA/I2S_CKIN/UART5_CTS/PSSI_D3/SDMMC1_D1/DCMIPP_D3
SDIO_D2		PC10	B14	PC10/TIM1_BKIN/SPI3_SCK/I2S3_CK/USART3_TX/UART4_TX/PSSI_D14/SDMMC1_D2/DCMIPP_D14
SDIO_D3		PC11	B15	PC11/SPI3_MISO/I2S3_SDI/USART3_RX/UART4_RX/PSSI_D4/SDMMC1_D3/DCMIPP_D4
SDIO_SCK		PC12	D12	PC12/TRACED3/TIM1_CH4/TIM15_CH1/SPI6_SCK/I2S6_CK/SPI3_MOSI/I2S3_SDO/USART3_CK/UART5_TX/PSSI_D9/SDMMC1_CK/DCMIPP_D9
	WK_UP	PC13	E4	PC13/TAMP_IN1/TAMP_OUT2/RTC_OUT1/RTC_TS/WKUP3

STM32H7R7L8H6H

Title: ATK-CN7RX.PrjPcb		
Author: ALIENTEK	Size: A4	
Date: 2024/11/21/星期四	File: STM32H7RX_CORE_ABC.SchDoc	
Revision: 0	Version: V1.0	

正点原子

MCU_DEFG

UIC

UART4_RX	PD0	C13	PD0/PSSI_DE/FMC_A22/UART4_RX/FDCAN1_RX/FMC_A6/DCMIPP_HSYNC
UART4_TX	PD1	B13	PD1/FMC_A23/UART4_TX/FDCAN1_TX/FMC_A7
SDIO_CMD	PD2	A14	PD2/TRACED2/TIM1_ETR/TIM3_ETR/TIM15_BKIN/PSSI_D11/UART5_RX/SDMMC1_CMD/DCMIPP_D11
SPI2_SCK	PD3	A12	PD3/TIM1_CH3N/SPI2_SCK/I2S2_CK/PSSI_D5/USART2_CTS/USART2_NSS/FMC_NWAIT/DCMIPP_D5/LCD_B1
T_PEN	PD4	C11	PD4/DCMIPP_HSYNC/PSSI_DE/USART2_RTS/ETH_PHY_INTN/FMC_NL/TAMP_IN6/TAMP_OUT3
USART2_TX	PD5	D7	PD5/TIM1_CH4N/DCMIPP_PIXCLK/PSSI_PDCK/USART2_TX/FMC_NCE/FMC_NE2/TAMP_IN5/TAMP_OUT4
USART2_RX	PD6	E7	PD6/SAI1_D1/ADF1_DATIN0/ETH_CLK/SPI3_MOSI/I2S3_SDO/SAI1_SCK_A/USART2_RX/PSSI_D10/FMC_INT/SDMMC2_CK/FMC_NE3/DCMIPP_D10
RMII_REF_CLK	PD7	B6	PD7/ETH_MII_RX_CLK/ETH_RMII_REF_CLK/SPI1_MOSI/I2S1_SDO/PSSI_D2/USART2_CK/SPDIFRX_IN1/SDMMC2_CMD/FMC_D8/FMC_AD8/DCMIPP_D2
LCD_DE	FMC_NE1	PD12	PD12/LPTIM1_IN1/TIM4_CH1/LPTIM2_IN1/USART3_RTS/PSSI_D12/SAI2_FS_A/FMC_NE1/DCMIPP_D12/LCD_DE
LED1	PD13	P14	PD13/LPTIM1_CH1/TIM4_CH2/UCPD_FRSTX2/SAI2_SCK_A/PSSI_D13/FMC_INT/DCMIPP_D13
LED0	PD14	L14	PD14/LPTIM1_CH2/TIM4_CH3/LPTIM2_CH1/DCMIPP_D7/UCPD_FRSTX1/UART8_CTS/PSSI_D7/FMC_D17
LCD_BL	PD15	L13	PD15/TIM4_CH4/LPTIM5_OUT/DCMIPP_D9/UCPD_FRSTX2/UART8_RTS/PSSI_D9/FMC_D18
DCMIPP_D2	PE0	A2	PE0/LPTIM1_ETR/TIM4_ETR/LPTIM2_ETR/UART8_RX/PSSI_D2/SAI2_MCLK_A/FMC_D9/FMC_AD9/DCMIPP_D2/TAMP_IN4/TAMP_OUT5
DCMIPP_D3	PE1	C3	PE1/LPTIM1_IN2/LPTIM2_CH2/UART8_TX/PSSI_D3/FMC_D10/FMC_AD10/DCMIPP_D3/TAMP_IN3/TAMP_OUT6
DCMIPP_RESET	PE2	B2	PE2/TRACECLK/ADF1_CCK0/SAI1_CK1/LPTIM5_IN1/SPI4_SCK/SAI1_MCLK_A/ETH_MII_TXD3/FMC_D11/FMC_AD11/TIM1_CH2N
IWIRE_DQ	IIC_INT	PE3	PE3/TRACED0/LPTIM5_ETR/TIM15_BKIN/SAI1_SD_B/ETH_MII_RXD3/FMC_D12/FMC_AD12
DCMIPP_D4	PE4	E2	PE4/TRACED1/SAI1_D2/ADF1_DATIN0/TIM15_CH1N/SPI4_NSS/SAI1_FS_A/PSSI_D4/FMC_D13/FMC_AD13/DCMIPP_D4
BEEP	PE5	F4	PE5/TRACED2/ADF1_CCK1/SAI1_CK2/TIM15_CH1/SPI4_MISO/SAI1_SCK_A/PSSI_D6/FMC_D14/FMC_AD14/DCMIPP_D6
SAI1_SDA	PE6	E1	PE6/TRACED3/TIM1_BKIN2/SAI1_D1/ADF1_DATIN0/TIM15_CH2/SPI4_MOSI/SAI1_SD_A/PSSI_D7/SAI2_MCLK_B/FMC_D15/FMC_AD15/DCMIPP_D7
UART7_RX	PE7	M6	PE7/TIM1_ETR/UART7_RX/FMC_A20/SAI2_SD_B/FMC_A4
UART7_TX	PE8	R4	PE8/TIM1_CH1N/UART7_TX/FMC_A12
KEY0	PE9	P5	PE9/TIM1_CH1/UART7_RTS/FMC_A14/FMC_BA0
FMC_A15	PE10	R5	PE10/TIM1_CH2N/UART7_CTS/FMC_A15/FMC_BA1
LCD_VSYNC	PE11	C12	PE11/TIM1_CH2/SPI4_NSS/SAI2_SD_B/LCD_VSYNC/FMC_SDNWE
T_SCK	PE12	E11	PE12/TIM1_CH3N/SPI4_SCK/SAI2_SCK_B/FMC_SDNRAS
T_MISO	PE13	B12	PE13/TIM1_CH3/SPI4_MISO/SAI2_FS_B/FMC_SDNCA5
T_MOSI	PE14	A13	PE14/TIM1_CH4/GFXTIM_FCLKCAL/SPI4_MOSI/SAI2_MCLK_B/FMC_SDNE0/GFXTIM_LCKCAL
T_CS	PE15	D11	PE15/TIM1_BKIN/GFXTIM_LCKCAL/FMC_SDCKE0/GFXTIM_FCLKCAL
IIC_SDA	PF0	B5	PF0/I2C2_SDA/LCD_R2/FMC_A8
IIC_SCL	PF1	C5	PF1/I2C2_SCL/FMC_A9
NRF_IRQ	GBC_KEY	PF2	PF2/I2C2_SMBA/DCMIPP_D14/PSSI_D14/FMC_A10
GBC_LED	PF3	A3	PF3/DCMIPP_D9/PSSI_D9/ETH_MII_CRS/FMC_A11
KEY1	PF4	B4	PF4/DCMIPP_D8/PSSI_D8/ETH_MII_TX_ER/FMC_A13
KEY2	PF5	G3	PF5/DCMIPP_D15/UCPD_UCPDFRSTX1/PSSI_D15/FMC_CLE/ETH_MII_RXD2/FMC_A16
SAI1_SDB	PF6	H5	PF6/TIM16_CH1/SPI5_NSS/SAI1_SD_B/UART7_RX/FMC_ALE/FMC_A17
SAI1_MCLKB	PF7	H4	PF7/TIM17_CH1/SPI5_SCK/SAI1_MCLK_B/UART7_TX/FMC_A18/LCD_G0
SAI1_SCKB	PF8	H3	PF8/TIM16_CH1N/DCMIPP_PIXCLK/SPI5_MISO/SAI1_SCK_B/UART7_RTS/PSSI_PDCK/TIM13_CH1/FMC_A19/LCD_G1
SAI1_FSB	PF9	J2	PF9/TIM17_CH1N/SPI5_MOSI/SAI1_FS_B/UART7_CTS/TIM14_CH1/FMC_A21/LCD_R0
NRF_CE	PF10	J3	PF10/TIM16_BKIN/SAI1_D3/DCMIPP_D15/PSSI_D15/PSSI_D11/FMC_A22/DCMIPP_D11/LCD_R1
RS485_RE	PF11	M5	PF11/SPI5_MOSI/PSSI_D12/SAI2_SD_B/FMC_A23/DCMIPP_D12/LCD_B0/ADC1_INP2
SPI5_MISO	PF12	R3	PF12/USART1_RX/SPI5_MISO/FMC_D19/ADC1_INP6/ADC1_INN2
SDNAND_CS	PF13	P3	PF13/USART1_TX/SPI5_NSS/PSSI_D10/FMC_D20/DCMIPP_D10/ADC2_INP2
SPI5_MOSI	PF14	P4	PF14/USART1_CTS/SPI5_MOSI/FMC_A24/LCD_G0/ADC2_INP6/ADC2_INN2
SPI5_SCK	PF15	N5	PF15/USART1_RTS/SPI5_SCK/FMC_A25/LCD_G1
LCD_R7	PG0	B11	PG0/TIM1_CH4N/LCD_R7
LCD_R6	PG1	E10	PG1/LCD_R6
LCD_HSYNC	PG2	C6	PG2/LCD_HSYNC
DCMIPP_HSYNC	PG3	A5	PG3/DCMIPP_HSYNC/PSSI_DE/ETH_PPS_OUT/FMC_D21
SPDIF_RX	RMII_TX_EN	PG11	PG11/LPTIM1_IN2/SPI1_SCK/I2S1_CK/SPDIFRX_IN1/PSSI_D3/SDMMC2_D2/ETH_MII_TX_EN/ETH_RMII_TX_EN/FMC_D28/DCMIPP_D3
RMII_TXD1	PG12	F2	PG12/LPTIM1_IN1/SPI6_MISO/I2S6_SD/SPDIFRX_IN2/SDMMC2_D3/ETH_MII_TXD1/ETH_RMII_TXD1/FMC_D29/LCD_G1
RMII_TXD0	PG13	G5	PG13/TRACED0/LPTIM1_CH1/SPI6_SCK/I2S6_CK/SDMMC2_D6/ETH_MII_TXD0/ETH_RMII_TXD0/FMC_D30/LCD_CLK
ETH_RESET	PG14	F1	PG14/TRACED1/LPTIM1_ETR/SPI6_MOSI/I2S6_SDO/SDMMC2_D7/ETH_MII_TXD1/ETH_RMII_TXD1/FMC_D31
NRF_CS	PG15	G4	PG15/LPTIM1_CH2/PSSI_D13/FMC_NBL3/DCMIPP_D13

STM32H7R7L8H6H

Title: ATK-CN7R7X.PrjPcb		
Author: ALIENTEK	Size: A4	
Date: 2024/11/21/星期四	File: STM32H7R7X_CORE_DEFG.SchDoc	
Revision: 0	Version: V1.0	

MCU_MNOP

UID

USB_PD_CC1	PM0	D10
USB_PD_CC2	PM1	C10
DCMIPP_SDA	PM2	B10
DCMIPP_SCL	PM3	A10
USB_HS_D-	PM5	B9
USB_HS_D+	PM6	A9
DCMIPP_PWDN	PM8	C9
USB_PWR	PM9	B8
USB_FS_D+	PM11	B7
USB_FS_D-	PM12	A7
	PM13	C7
	PM14	C8

XSPI2_RWDS	PN0	G14
XSPI2_CS	PN1	F14
XSPI2_DQ0	PN2	H15
XSPI2_DQ1	PN3	G13
XSPI2_DQ2	PN4	K14
XSPI2_DQ3	PN5	K15
XSPI2_CLK	PN6	J14
XSPI2_NCLK	PN7	J13
XSPI2_DQ4	PN8	K13
XSPI2_DQ5	PN9	H14
XSPI2_DQ6	PN10	H13
XSPI2_DQ7	PN11	G15
XSPI2_RST	PN12	L15

XSPI1_CS	PO0	R13
	PO1	P7
XSPI1_DQS	PO2	N11
	PO3	M11
XSPI1_CLK	PO4	R10
	PO5	P10

XSPI1_DQ0	PP0	P11
XSPI1_DQ1	PP1	N12
XSPI1_DQ2	PP2	N9
XSPI1_DQ3	PP3	P9
XSPI1_DQ4	PP4	R8
XSPI1_DQ5	PP5	N10
XSPI1_DQ6	PP6	R11
XSPI1_DQ7	PP7	P12
	PP8	P13
	PP9	R14
	PP10	M12
	PP11	P6
	PP12	N6
	PP13	R7
	PP14	N7
	PP15	P8

STM32H7R7L8H6H

MOTHER BOARD CON

主板接口，由2个2*35P的B2B端子（公）组成，可插在正点原子STM32H7RX主板上。

由于核心板用的双槽B2B端子，接触十分紧密，连接可靠性更好，但是核心板不太好拔出来，如果蛮力拔，很容易崩坏，没经验几乎百分百会崩坏，如无必要，切勿自行拆卸核心板！如实在要拆请参考我们：H7RX核心板拆卸视频

PD5	1	70	PF1
PF2	2	69	PF0
PF2	3	68	PF4
PM14	4	67	VBAT
BOOT0	5	66	PB8
PM12	6	65	PE2
PM9	7	64	PE0
PA1	8	63	PB5
PA0	9	62	PB4
PB13	10	61	PB3
PB12	11	60	PA15
PB11	12	59	PG1
PB10	13	58	PG0
PA12	14	57	PM13
PA11	15	56	PG3
PA10	16	55	PM0
PA9	17	54	PM11
PA8	18	53	PM8
PA2	19	52	PM6
GND	20	51	PM5
PD7	21	50	PM1
GND	22	49	PM3
PA5	23	48	PM2
GND	24	47	PD3
PC12	25	46	PD1
GND	26	45	PD2
PG2	27	44	PD0
PE11	28	43	PC10
PD12	29	42	PE15
PD15	30	41	PE14
PA14	31	40	PE13
PA13	32	39	PE12
PC11	33	38	PD4
PC7	34	37	PC8
PC6	35	36	PC9

LB340-G70P-B0R

RESET	1	70	GND
PD14	2	69	PD6
PB6	3	68	PB7
PC13	4	67	PB15
PE3	5	66	PB14
PE5	6	65	PD13
PG13	7	64	PP9
PP10	8	63	PP8
PO3	9	62	PO5
PP13	10	61	PP15
PP11	11	60	PO1
PE9	12	59	PF6
PE10	13	58	PF7
PE8	14	57	PF8
PP14	15	56	PG15
PE7	16	55	PF5
PP12	17	54	PG11
PF11	18	53	PG12
GND	19	52	PC4
PB0	20	51	PB2
GND	21	50	PB1
PA6	22	49	PA3
GND	23	48	PC2
PA7	24	47	PC1
PC5	25	46	PG14
PA4	26	45	PC3
PF10	27	44	PC0
PB9	28	43	PF9
PE1	29	42	PE4
GND	30	41	PE6
VREF+	31	40	VCC3.3
GND	32	39	VCC3.3
GND	33	38	VCC5
GND	34	37	VCC5
GND	35	36	VCC5

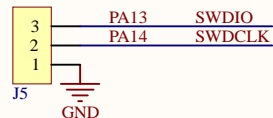
LB340-G70P-B0R

Title:
ATK-CN7H7RX.PrjPcb
Author:
ALIENTEK
Date:
2024/11/21/星期四
Revision:
0

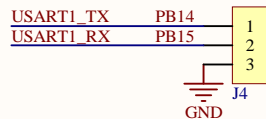
Size:
A4
File:
STM32H7RX_CORE_MNOP.SchDoc
Version:
V1.0



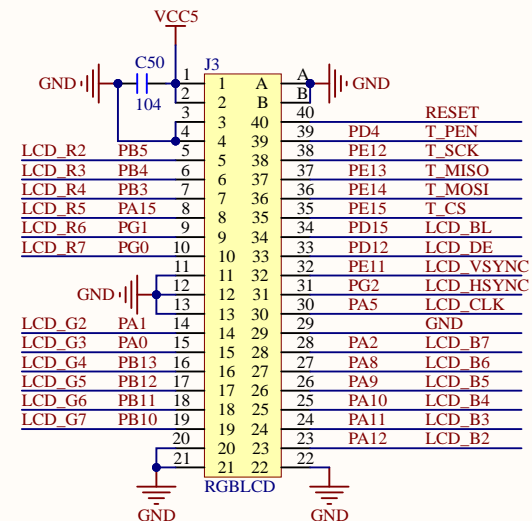
SWD



USART1



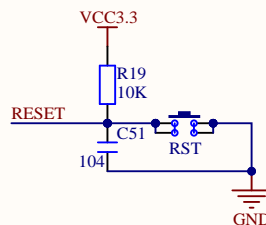
RGB LCD



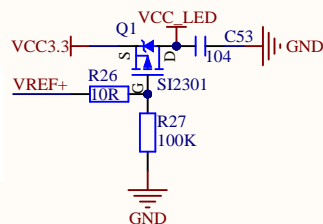
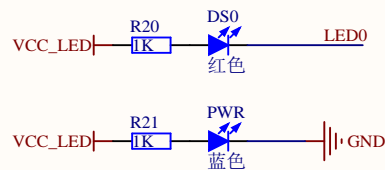
KEY



RESET



LED



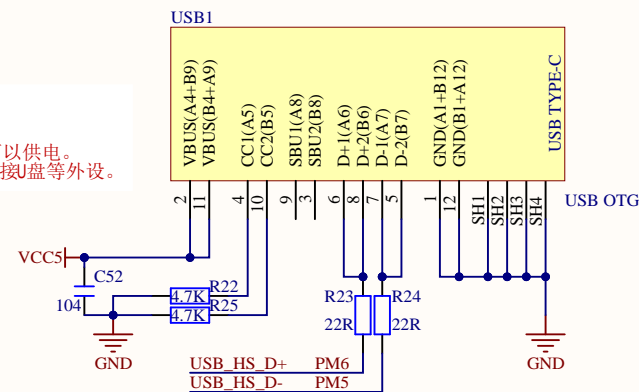
DS0和PWR这两个LED受VREF+的控制：
如果VREF+为高电平，则DS0和PWR都不亮。
如果VREF+为低电平，则DS0和PWR正常工作。

注意：通过主板上的P5可以控制VREF+。

Type-C USB

此USB接口有如下功能：

1. 单独使用核心板时，可给核心板供电。
2. 可做USB Slave接口，连接电脑，同时也可以供电。
3. 可做USB Host接口（需Type C转OTG线），接U盘等外设。

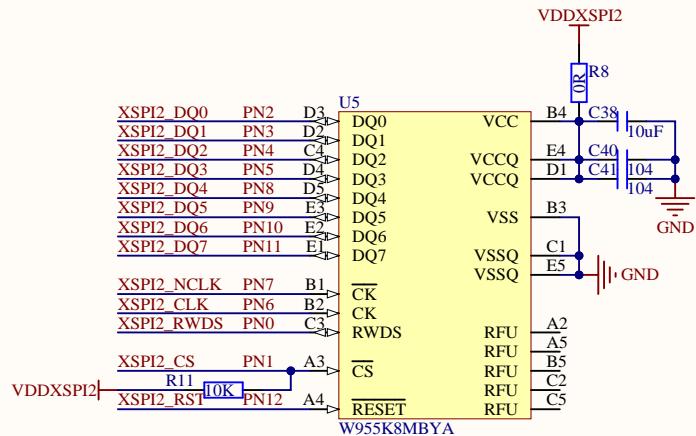


Title: ATK-CN7RX.PrjPcb	
Author: ALIENTEK	Size: A4
Date: 2024/11/21/星期四	File: STM32H7RX_DEVICE.SchDoc
Revision: 0	Version: V1.0

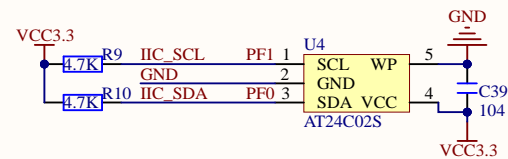


正点原子

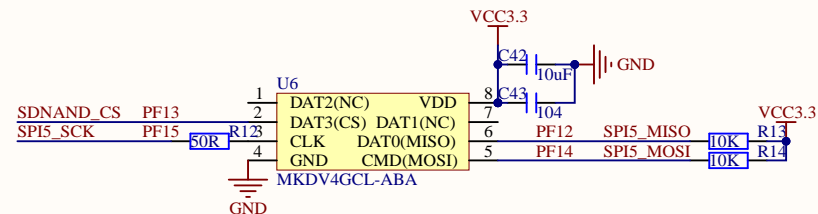
Hyper RAM



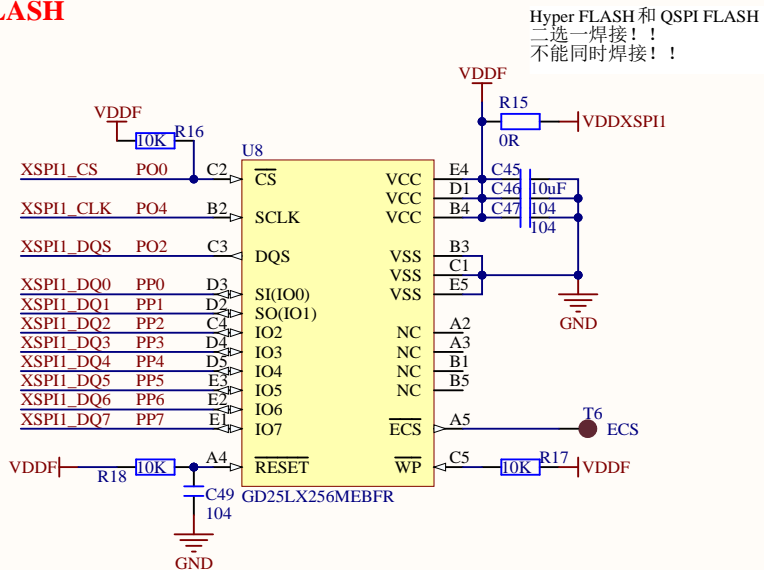
EEPROM



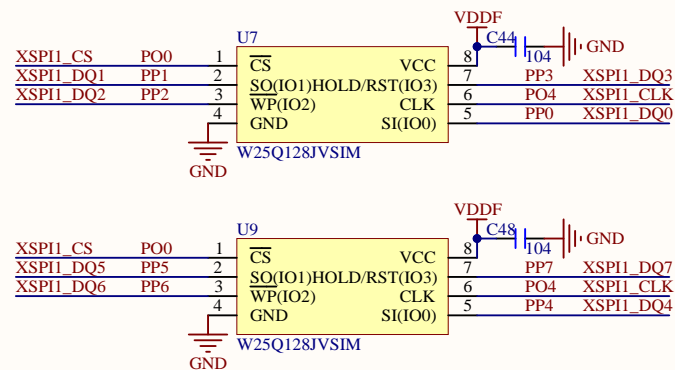
SD NAND



Hyper FLASH



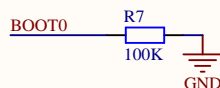
QSPI FLASH



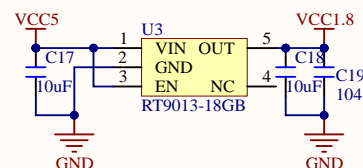
Title: ATK-CN7RX.PrjPcb	
Author: ALIENTEK	Size: A4
Date: 2024/11/21/星期四	File: STM32H7RX_MEMORY.SchDoc
Revision: 0	Version: V1.0



1



A circuit diagram showing a blue line representing the BOOT0 pin. This line passes through a blue rectangular component labeled 'R7' with '100K' written below it. After the resistor, the line continues to a ground symbol, which is a horizontal line with three downward-pointing lines of decreasing width below it, labeled 'GND'.



Title: ATK-CNH7RX_PriPcb	
Author: ALIENTEK	Size: A4
Date: 2024/11/21/星期四	File: STM32H7RX_POWER_SchDoc
Revision: 0	Version: V1.0



